

ISSUE CLASSIFICATION	
Class	Subclass

~~PATENT NUMBER~~

U.S. ~~UTILITY~~ Patent Application

502
50258

SCANNED **O.I.P.E.**

PATENT DATE

APPLICATION NO. 09/749750	CONT/PRIOR	CLASS 711	SUBCLASS 154	ART UNIT 2185 2186	EXAMINER S. Elmore
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TITLE	APPLICANTS
1. Chairman	1. Chairman
2. Vice Chairman	2. Vice Chairman
3. Secretary	3. Secretary
4. Treasurer	4. Treasurer
5. Member	5. Member
6. Member	6. Member
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100. Member	100. Member

Subramaniam Maiyuran
Lyman Moulton

Low power cache architecture

PTD-2040
12/09

ISSUING CLASSIFICATION									
ORIGINAL				CROSS REFERENCE(S)					
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION									

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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